

SYSTEM FOR REORDERING SEQUENCED BASED PACKETS IN A SWITCHING NETWORK

ABSTRACT OF THE INVENTION

5 System for reordering sequenced based packets in a switching network. The system includes a plurality of source processors that transmit the packets to a destination processor via multiple communication fabrics. The source processors and the destination processor are synchronized together. Time stamp logic at each source processor operates to include a time stamp parameter with each of the packets transmitted from the source

10 processors. The system also includes a plurality of memory queues located at the destination processor. An Enqueue processor operates to store a memory pointer and an associated time stamp parameter for each of the packets received at the destination processor in a selected memory queue. A Dequeue processor determines a selected memory pointer associated with a selected time stamp parameter and operates to process

15 the selected memory pointer to access a selected packet for output in a reordered packet stream.